



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/717,671
Applicant : Anuj DUA, et al.
Filed : November 21, 2003
Title : A METHOD AND APPARATUS FOR FETCHING
INSTRUCTIONS FROM THE MEMORY SUBSYSTEM OF A
MIXED ARCHITECTURE PROCESSOR INTO A HARDWARE
EMULATION ENGINE
TC/A.U. : 2183
Examiner : Huisman, David J.
Docket No. : 10001618-3
Customer No. : 022879

Mail Stop Amendment

Commissioner of Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

**SUPPLEMENTAL AMENDMENT TO ACCOMPANY A REQUEST FOR
CONTINUED EXAMINATION**

Sir:

In further response to the November 16, 2006 Office Action, and in response to the March 19, 2007 Notice of Non-Compliant Amendment, please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this response.

Remarks begin on page 5 of this response.